

Notice of References Cited	Application/Control No. 10/708,936	Applicant(s)/Patent Under Reexamination CHASE ET AL.	
	Examiner Heather A. Doty	Art Unit 2813	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,723,250	04-2004	Schaefer et al.	216/2
*	B	US-6,913,701	07-2005	Moon et al.	216/2
*	C	US-6,845,664	01-2005	Okojie, Robert S.	73/431
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, Vol. 1, Lattice Press, 2000, pp. 265, 268.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.